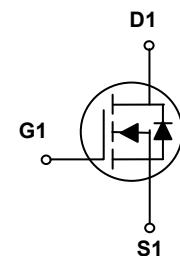
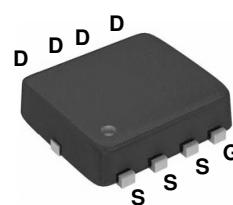


Main Product Characteristics

BV _{DSS}	40V
R _{DS(ON)}	7.2mΩ
I _D	45A



PPAK3X3

Schematic Diagram



Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

Description

The GSFP0446 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	+20/-12	V
Drain Current-Continuous (T _C =25°C)	I _D	45	A
Drain Current-Continuous (T _C =100°C)		28	
Drain Current-Pulsed ¹	I _{DM}	180	A
Single Pulse Avalanche Energy ²	E _{AS}	65	mJ
Single Pulse Avalanche Current ²	I _{AS}	36	A
Power Dissipation (T _C =25°C)	P _D	33	W
Power Dissipation-Derate above 25°C		0.26	W/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62	°C/W
Thermal Resistance, Junction-to-Case	R _{θJC}	3.84	°C/W
Operating Junction Temperature Range	T _J	-55 To +150	°C
Storage Temperature Range	T _{STG}	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On/Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{\text{DS}}=32\text{V}, V_{\text{GS}}=0\text{V}, T_J=100^\circ\text{C}$	-	-	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	100	nA
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=8\text{A}$	-	6	7.2	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=6\text{A}$	-	9.6	12.5	
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.6	2.5	V
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{S}}=1\text{A}$	-	5	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=25\text{A}$ $V_{\text{GS}}=10\text{V}$	-	13.7	20	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	2.2	5	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	3.7	7	
Turn-On Delay Time ^{3,4}	$t_{\text{d(on)}}$	$V_{\text{DD}}=20\text{V}, R_{\text{G}}=6\Omega$ $V_{\text{GS}}=10\text{V}, I_{\text{D}}=25\text{A}$	-	10	15	nS
Rise Time ^{3,4}	t_r		-	14	21	
Turn-Off Delay Time ^{3,4}	$t_{\text{d(off)}}$		-	28	42	
Fall Time ^{3,4}	t_f		-	20	30	
Input Capacitance	C_{iss}	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	930	1400	pF
Output Capacitance	C_{oss}		-	380	570	
Reverse Transfer Capacitance	C_{rss}		-	30	45	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	1	-	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_s	$V_G=V_D=0\text{V},$ Force Current	-	-	45	A
Pulsed Source Current	I_{SM}		-	-	90	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}, T_J=25^\circ\text{C}$	-	-	1	V
Reverse Recovery Time	t_{rr}	$V_R=30\text{V}, I_{\text{S}}=10\text{A}$ $di/dt=100\text{A}/\mu\text{s}$ $T_J=25^\circ\text{C}$	-	45	-	nS
Reverse Recovery Charge	Q_{rr}		-	35	-	nC

Note:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=36\text{A}, R_{\text{G}}=25\Omega$, starting $T_J=25^\circ\text{C}$.
3. Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

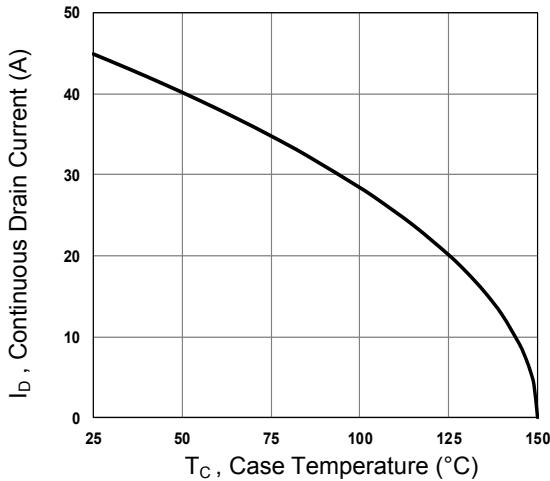


Figure 1. Continuous Drain Current vs. T_c

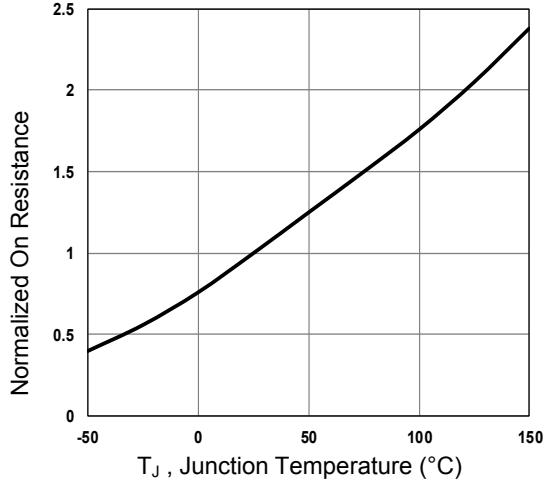


Figure 2. Normalized R_{DS(ON)} vs. T_j

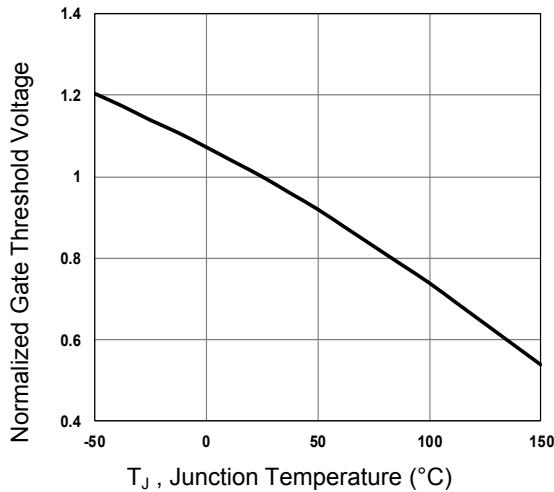


Figure 3. Normalized V_{th} vs. T_j

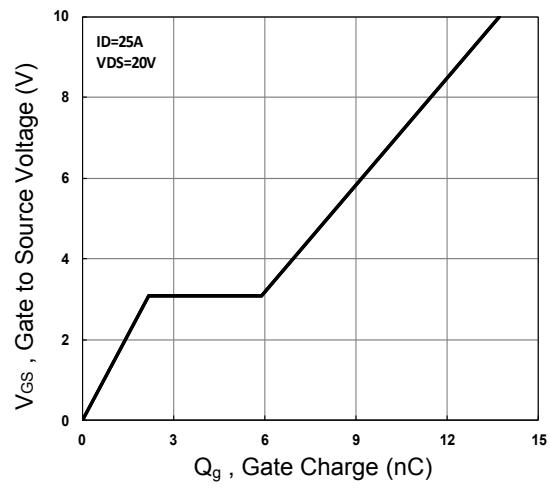


Figure 4. Gate Charge Waveform

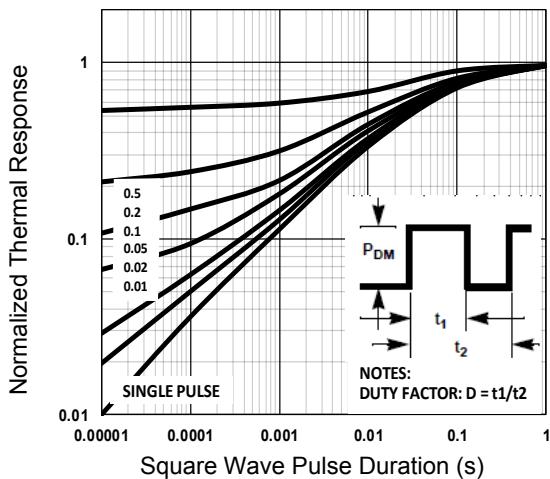


Figure 5. Normalized Transient Impedance

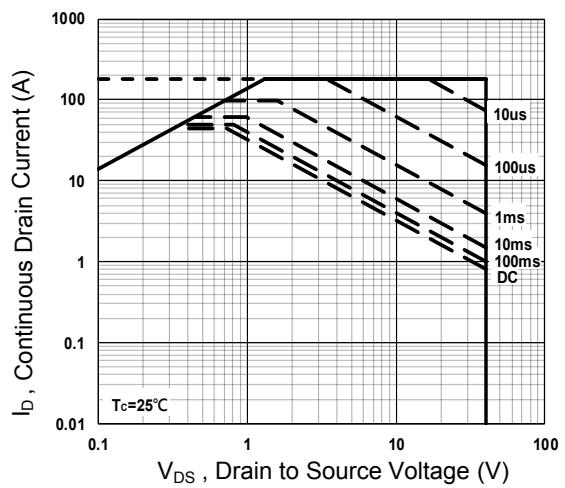


Figure 6. Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

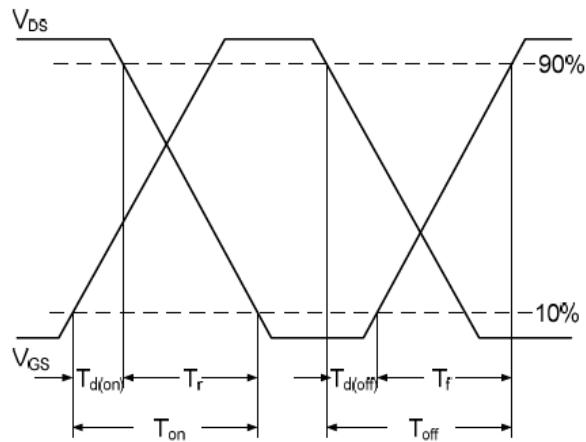


Figure 7. Switching Time Waveform

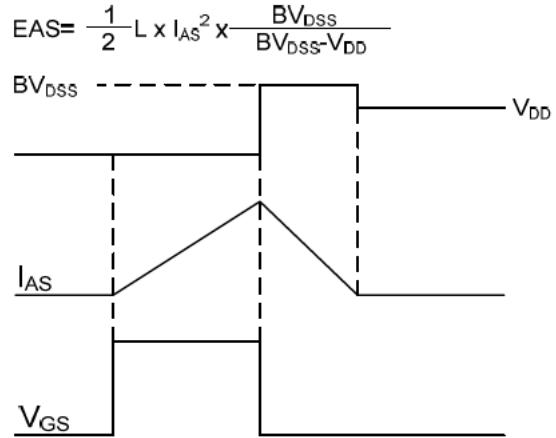
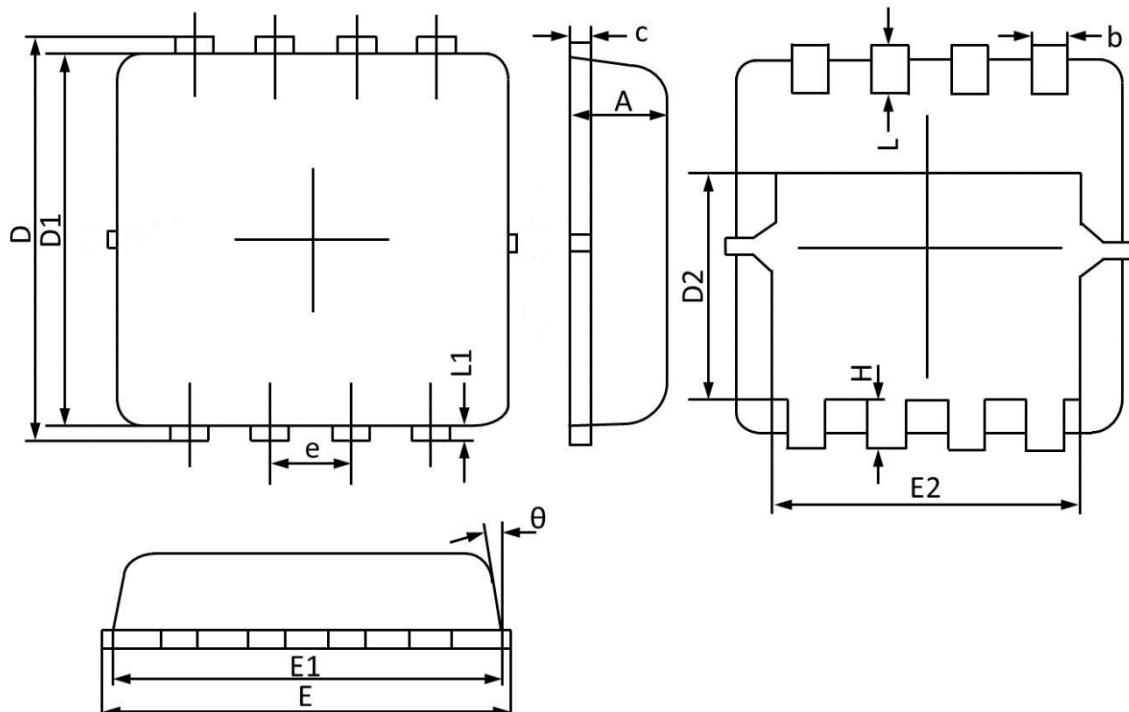


Figure 8. EAS Waveform

Package Outline Dimensions PPAK3X3



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°

Recommended Pad Layout

